

FIG. 1

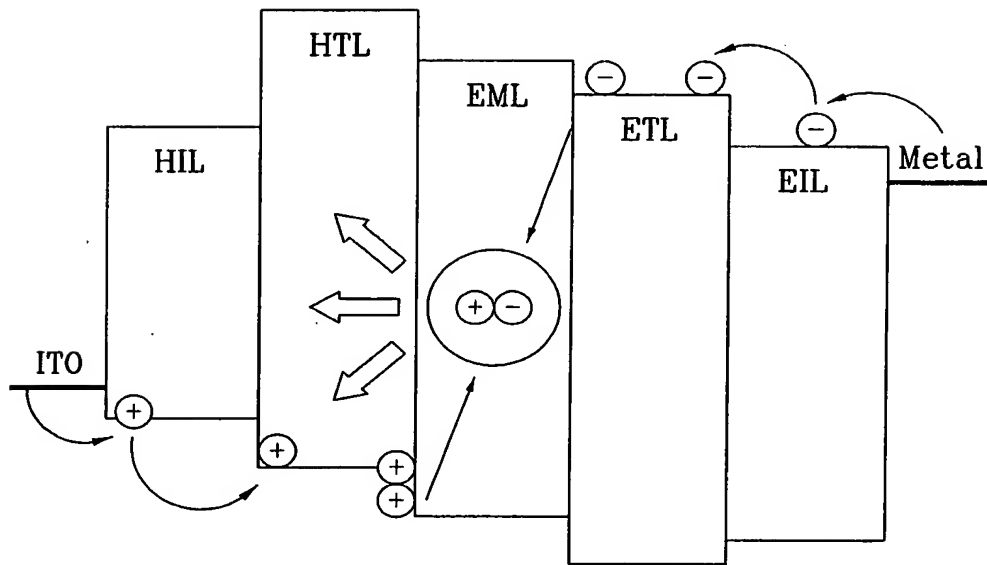


FIG. 2

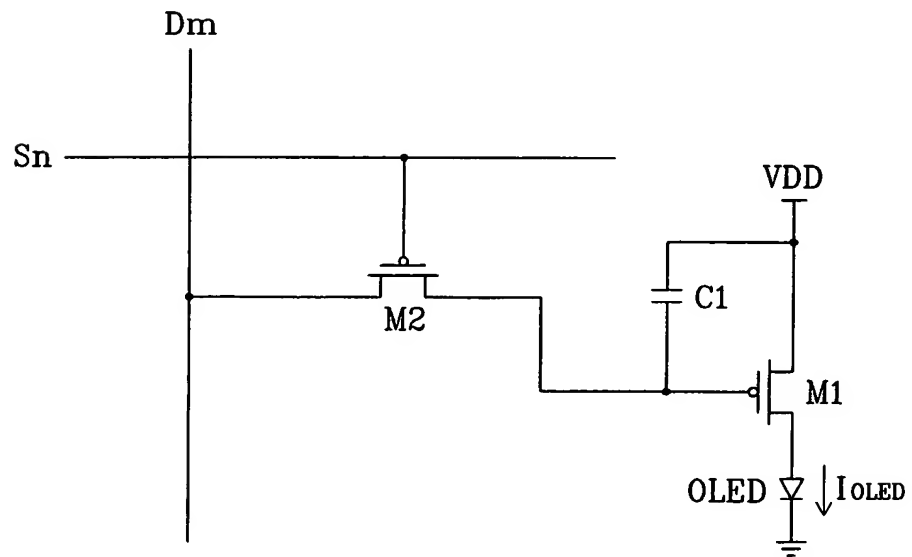


FIG. 3

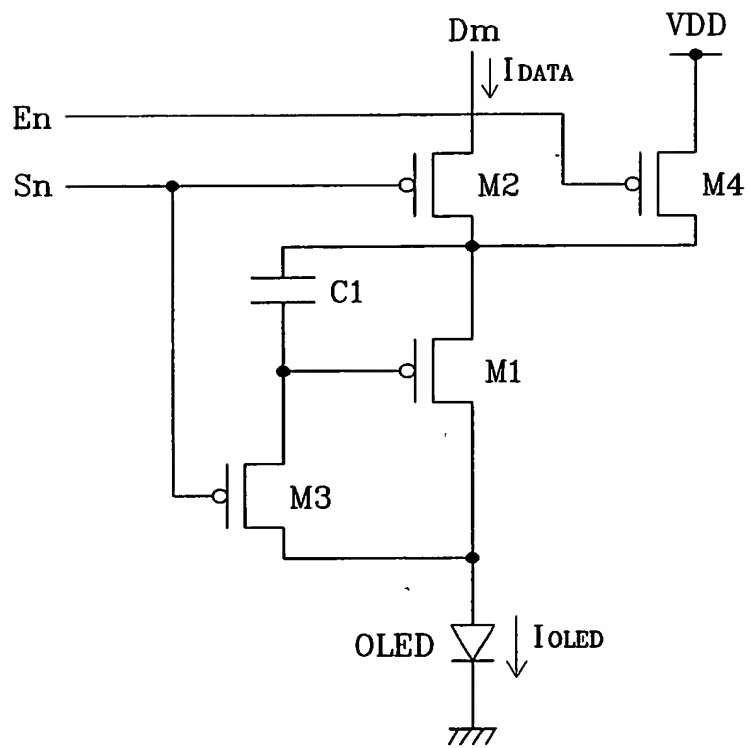
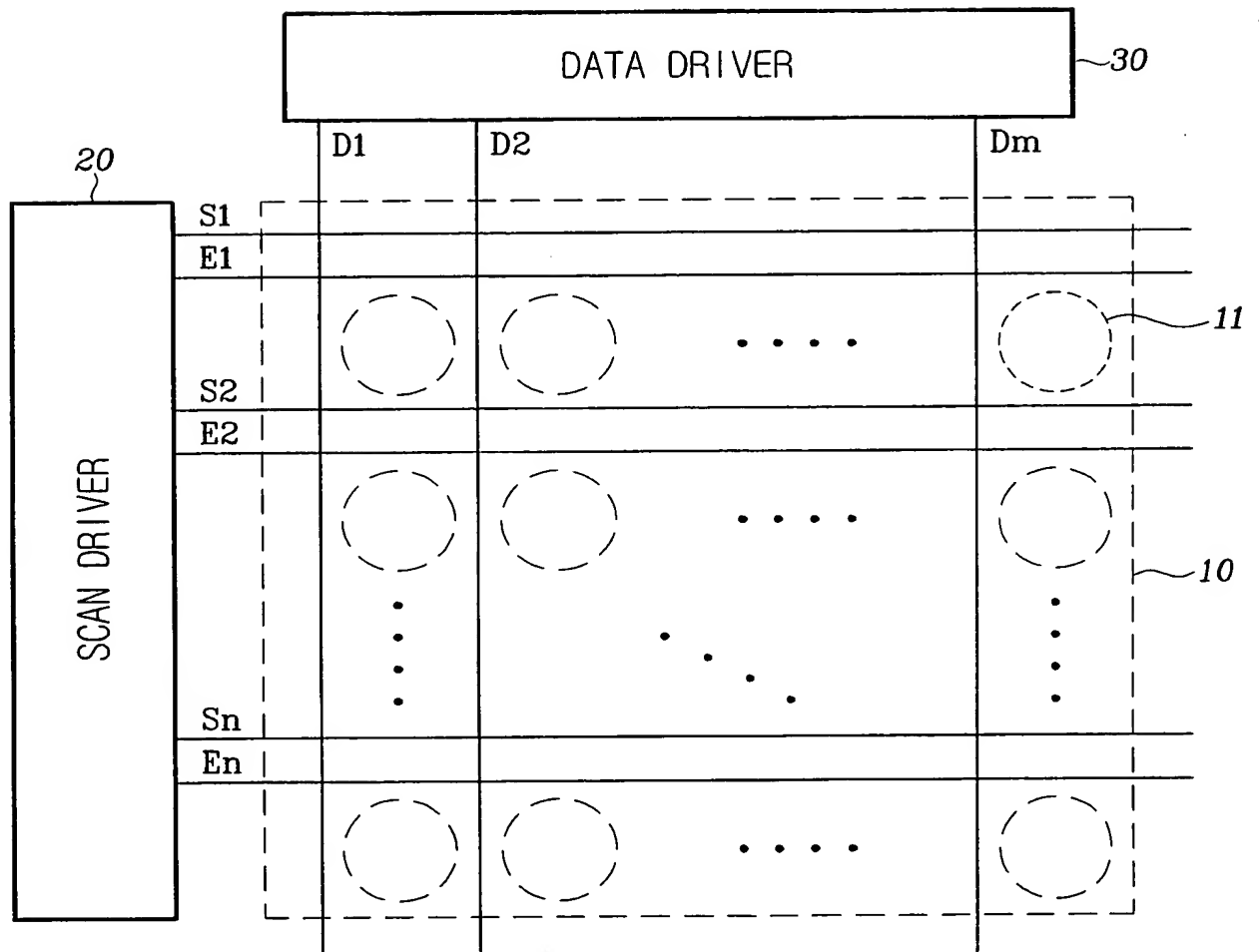


FIG. 4



[illegible]

Timing diagram for the data bus during the first data transfer. The diagram shows four signals:  $SEN$ ,  $SEN+1$ ,  $EMn$ , and  $I_{DATA}$  over three time intervals:  $T1$ ,  $T2$ , and  $T3$ .

- $SEN$ : High in  $T1$  and  $T2$ , Low in  $T3$ .
- $SEN+1$ : High in  $T1$  and  $T2$ , Low in  $T3$ .
- $EMn$ : High in  $T1$  and  $T2$ , Low in  $T3$ .
- $I_{DATA}$ : High in  $T1$  and  $T2$ , Low in  $T3$ .

FIG. 7

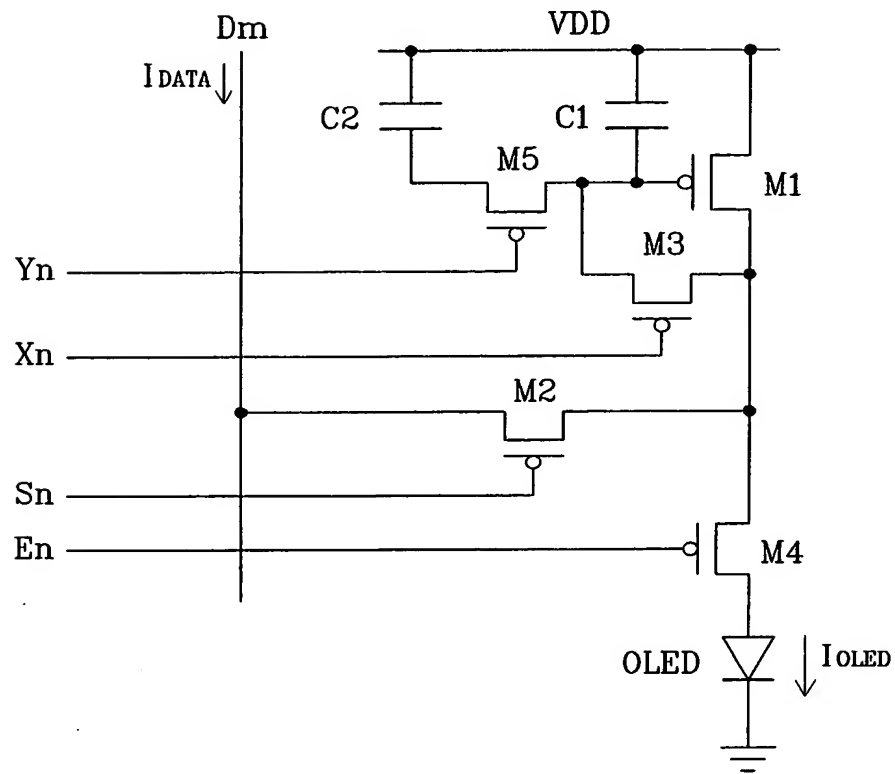
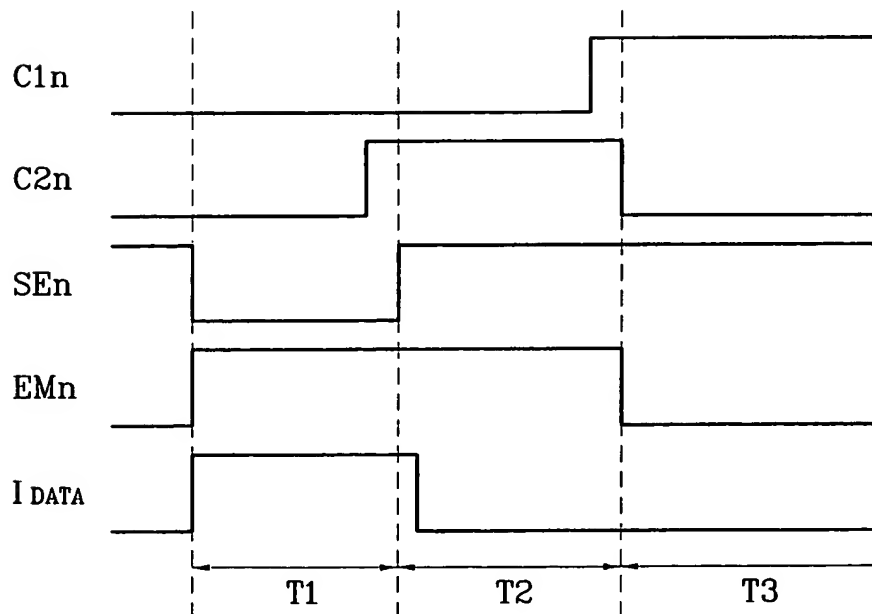


FIG. 8



[illegible]